

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims:

1-15. (Canceled)

16. (Currently Amended) A display drive system having ICs comprising a first IC and a second IC, each of said first IC and said second IC comprising:

a current mirror circuit having an input side transistor supplied with a predetermined drive current and a plurality of output side parallel transistors, from which the drive currents to be supplied to terminal pins of a display panel are derived; and

a current output circuit which generates a current having a first value in response to an output current of one of said output side parallel transistors;

wherein said second IC of said display drive system further comprises:

a current generator circuit which generates said predetermined drive current of said second IC in response to a predetermined input current and being supplied with said current having said first value of said current output circuit of said first IC.

17. (Currently Amended) The display drive system according to claim 16, wherein said current output circuit of said first IC generates the current having the first value in response to an output current of a certain output side transistor connected in parallel to said output side transistors of said current mirror circuit of said first IC.

18. (Previously Presented) The display drive system according to claim 16, wherein said current generator circuit includes a reference current generator circuit for generating said predetermined drive current as a predetermined reference current.

19. (Previously Presented) The display drive system according to claim 18, wherein the reference current generator circuit includes a first resistor, a second resistor, and an amplifier, said amplifier generating a current in response to a difference between voltages output across said first and second resistors as the predetermined reference current.

20. (Previously Presented) The display drive system according to claim 19, wherein the first and second resistors have substantially identical resistance values.

21. (Previously Presented) The display drive system according to claim 16, wherein the plurality of output side parallel transistors are P channel MOSFETs whose sources are connected to a power source line.

22. (Previously Presented) The display drive system according to claim 17, wherein said current generator circuit includes a reference current generator circuit for generating said predetermined drive current as a predetermined reference current.

23. (Previously Presented) The display drive system according to claim 22, wherein the reference current generator circuit includes a first resistor, a second resistor, and an amplifier, said amplifier generating a current in response to a difference between voltages output across said first and second resistors as the predetermined reference current.

24. (Previously Presented) The display drive system according to claim 23, wherein the first and second resistors have substantially identical resistance values.

25. (Currently Amended) A display drive system having ICs comprising a first IC and a second IC, each of said first IC and said second IC comprising:

current mirror circuit means having an input side transistor supplied with a predetermined drive current and a plurality of output side parallel transistors, for deriving drive currents that are supplied to terminal pins of a display panel; and

current output circuit means for generating a current having a first value in response to an output current of one of said output side parallel transistors;

wherein said second IC of said display drive system further comprises:

current generator circuit means for generating said predetermined drive current of said second IC in response to a predetermined input current and being supplied with said current having said first value of said current output circuit means of said first IC.

26. (Currently Amended) The display drive system according to claim 25, wherein said current output circuit means of said first IC generates the current having

the first value in response to an output current of a certain output side transistor connected in parallel to said output side transistors of said current mirror circuit means of said first IC.

27. (Previously Presented) The display drive system according to claim 25, wherein said current generator circuit means includes a reference current generator circuit means for generating said predetermined drive current as a predetermined reference current.

28. (Previously Presented) The display drive system according to claim 27, wherein the reference current generator circuit means includes a first resistor, a second resistor, and an amplifier, said amplifier generating a current in response to a difference between voltages output across said first and second resistors as the predetermined reference current.

29. (Previously Presented) The display drive system according to claim 28, wherein the first and second resistors have substantially identical resistance values.

30. (Previously Presented) The display drive system according to claim 25, wherein the plurality of output side parallel transistors are P channel MOSFETs whose sources are connected to a power source line.

31. (Previously Presented) The display drive system according to claim 26, wherein said current generator circuit means includes a reference current generator

circuit means for generating said predetermined drive current as a predetermined reference current.

32. (Previously Presented) The display drive system according to claim 31, wherein the reference current generator circuit means includes a first resistor, a second resistor, and an amplifier, said amplifier generating a current in response to a difference between voltages output across said first and second resistors as the predetermined reference current.

33. (Previously Presented) The display drive system according to claim 32, wherein the first and second resistors have substantially identical resistance values.

34. (Previously Presented) The display drive system according to claim 33, wherein the plurality of output side parallel transistors are P channel MOSFETs whose sources are connected to a power source line.